

70014

SEF 12 1997

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 08/530,661  
Filing Date ..... September 20, 1995  
Inventor ..... Brent Keeth  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2503  
Examiner ..... N. Kelly  
Attorney's Docket No. .... MI22-356  
Title: Semiconductor Memory Circuit

SUPPLEMENTAL RESPONSE TO MARCH 19, 1997 OFFICE ACTION

To: BOX NON-FEE AMENDMENT  
Assistant Commissioner For Patents  
Washington, D.C. 20231

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AMENDMENTS

In The Specification.

At p. 2, ln. 19, after "pitch", insert --of--.

At p. 9, ln. 10, replace "ares" with --areas--.

At p. 27, ln. 20, replace "result sin" with --results in--.